

FIGURE 1  
(PRIOR ART)

10

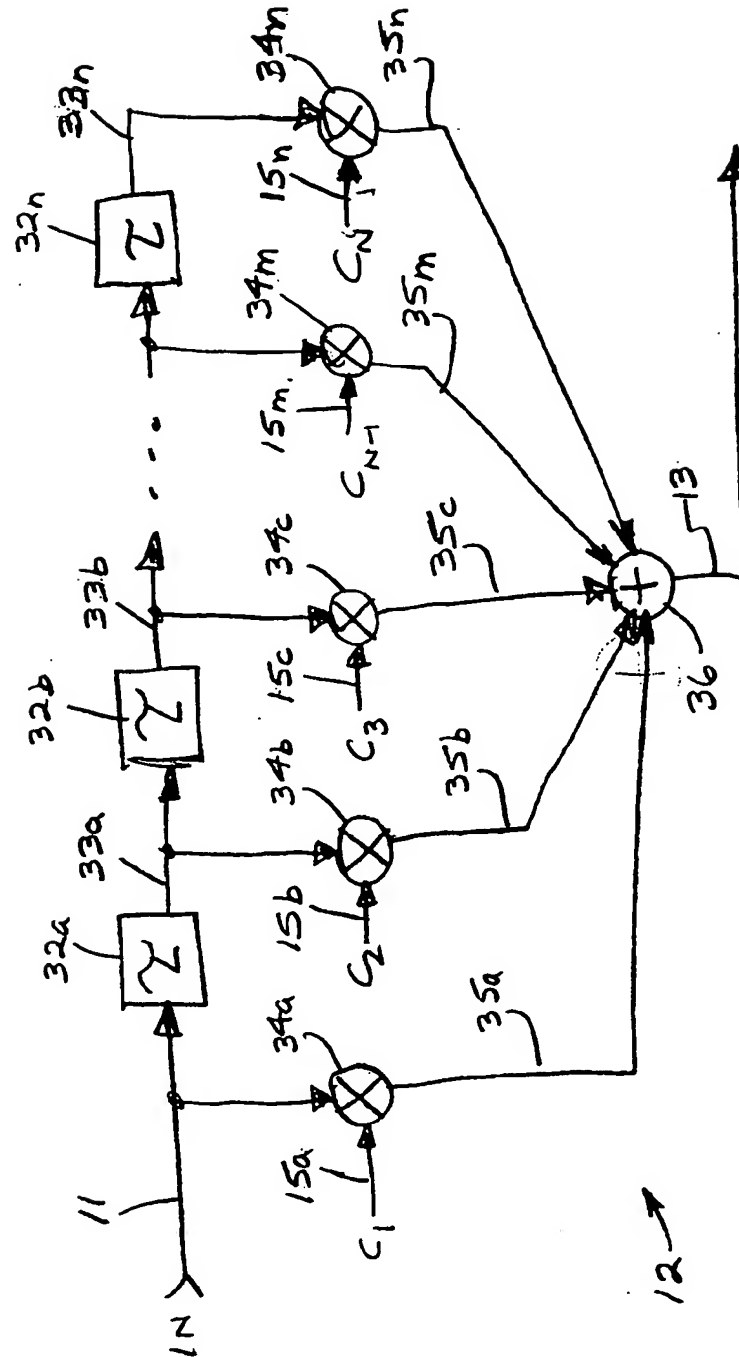


FIGURE 2  
(PRIOR ART)

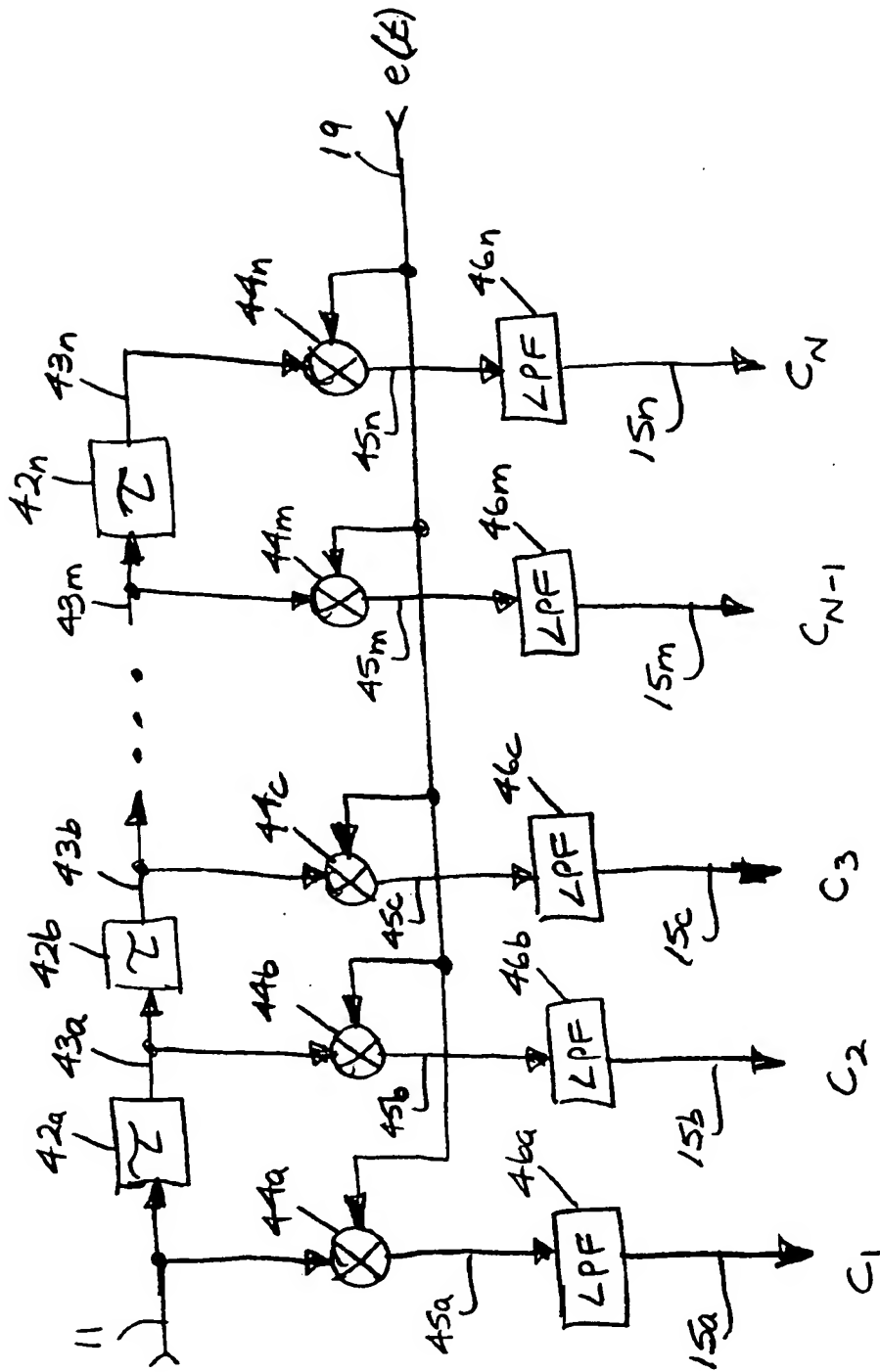
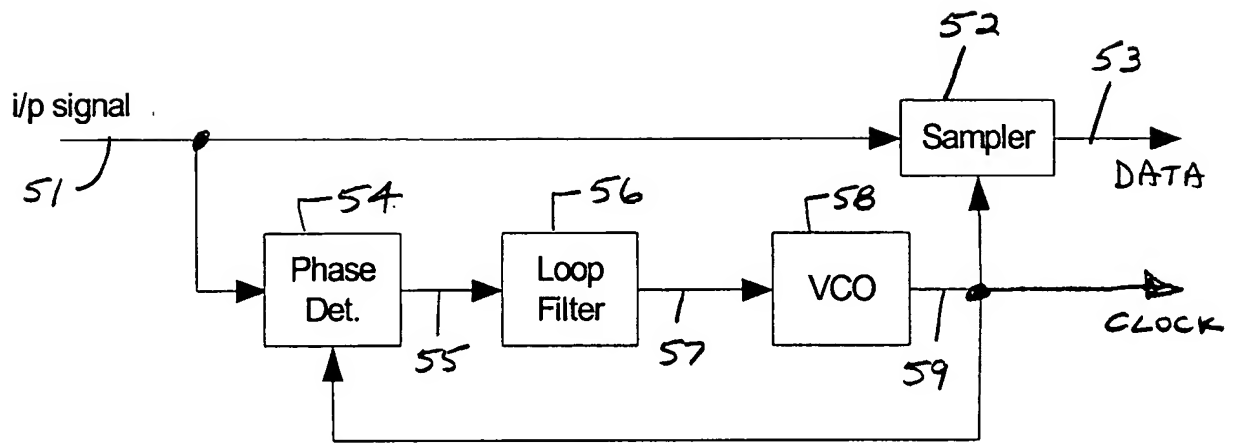


FIGURE 3  
(PRIOR ART)

14



50 →

FIGURE 4  
(PRIOR ART)

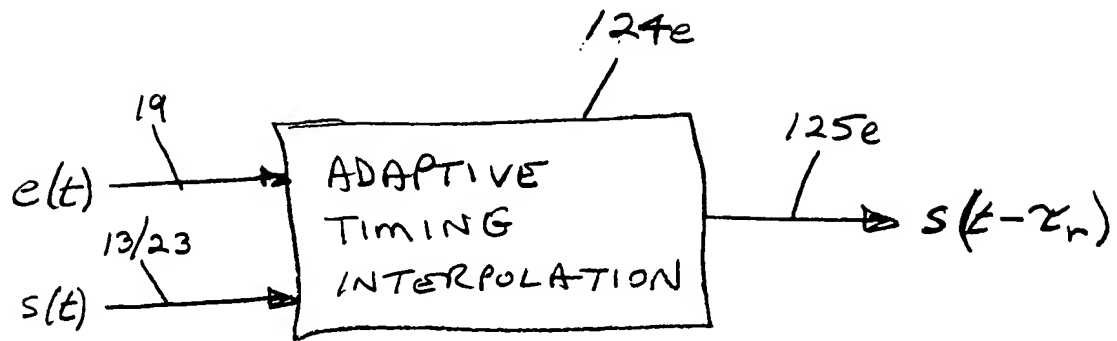


FIGURE 5

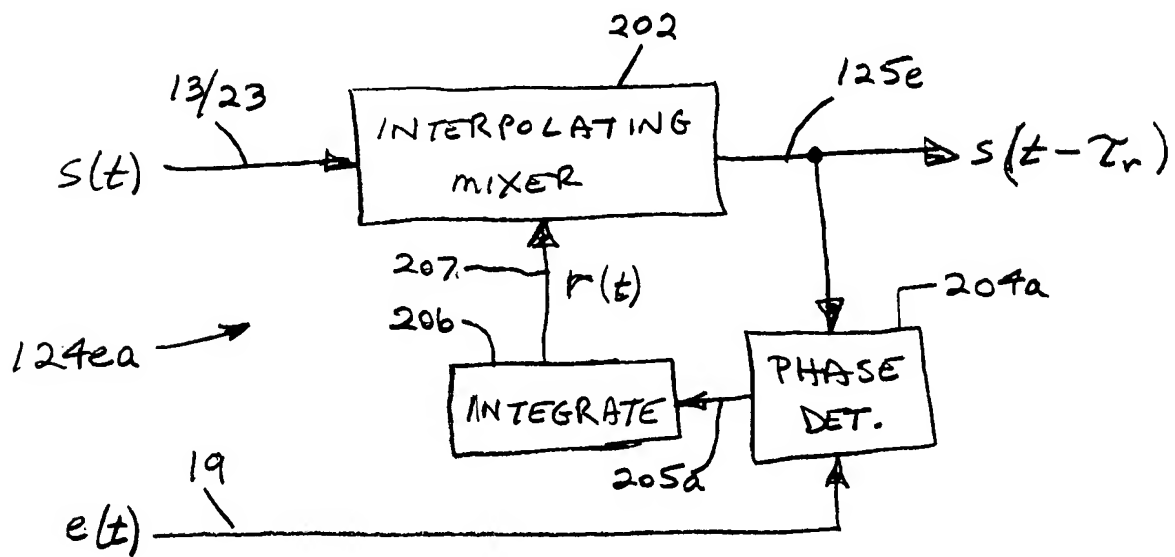


FIGURE 6

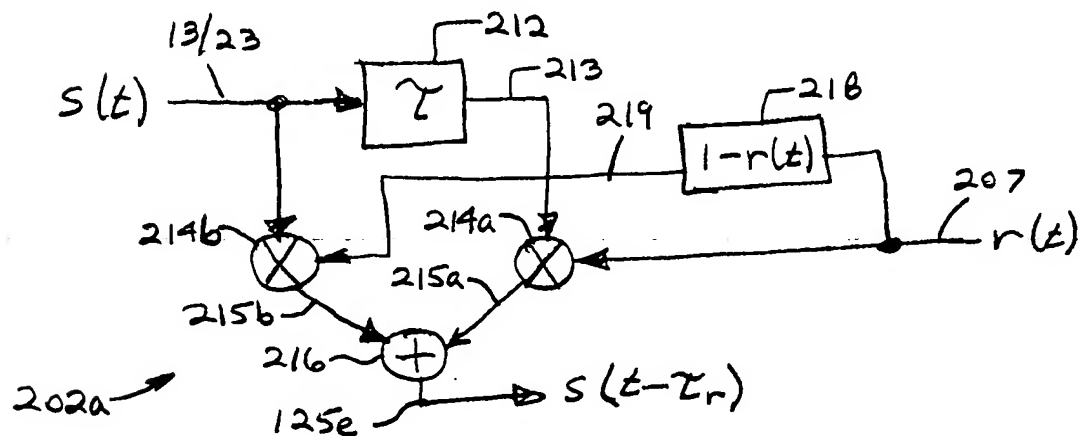


FIGURE 7

FIGURE 9

Block diagram of a feedback control system. The reference signal  $M_r$  (227) is compared with the feedback signal  $e(t)$  (19) at a summing junction (226). The resulting error signal (229) is fed into a differentiator block (228). The output of the differentiator is fed back to the summing junction. The output of the system is labeled 205b. A feedback path is also indicated by 204ba.

FIGURE 10A

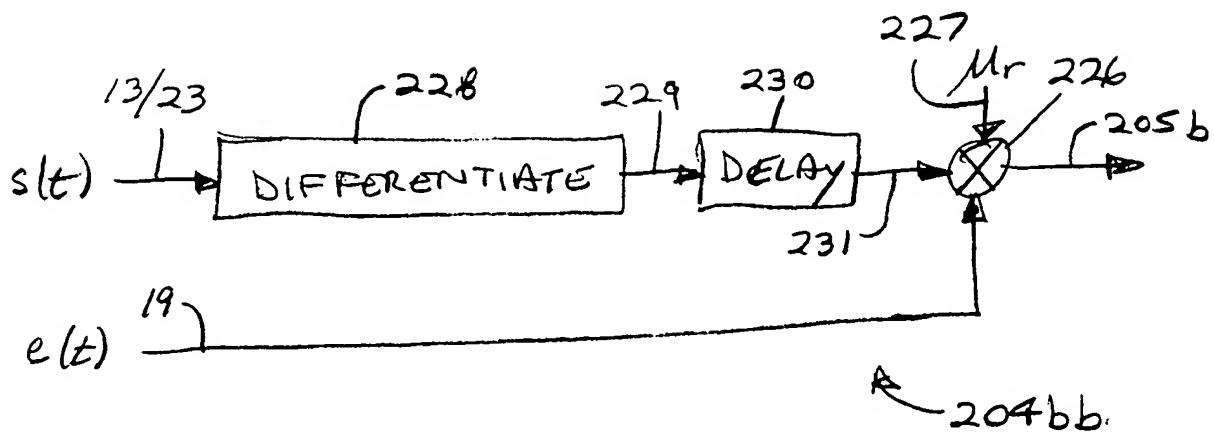


FIGURE 10B

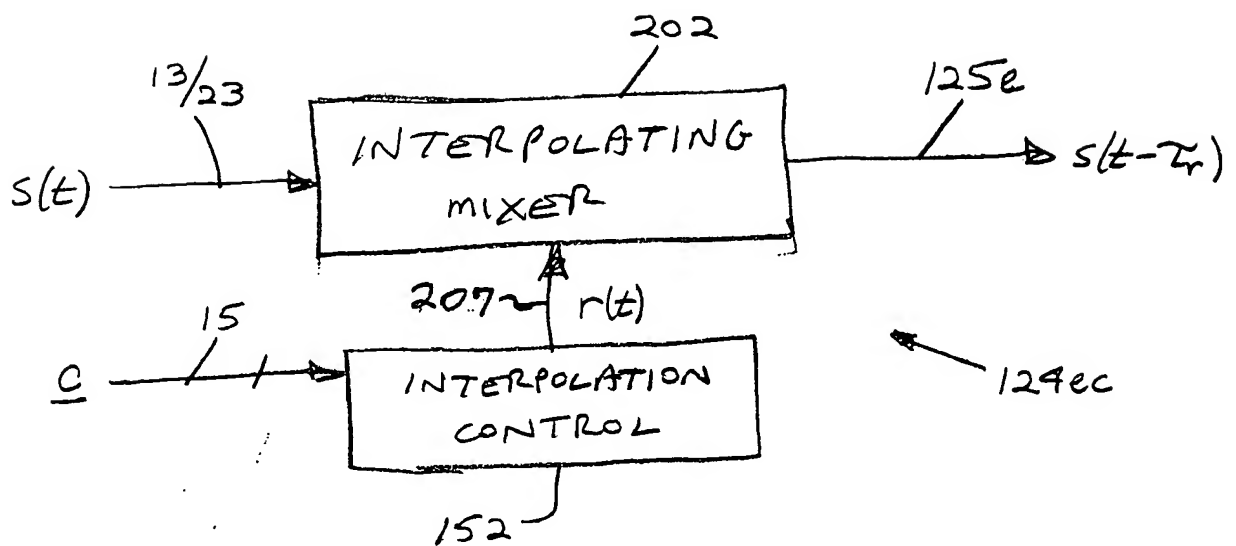


FIGURE 11

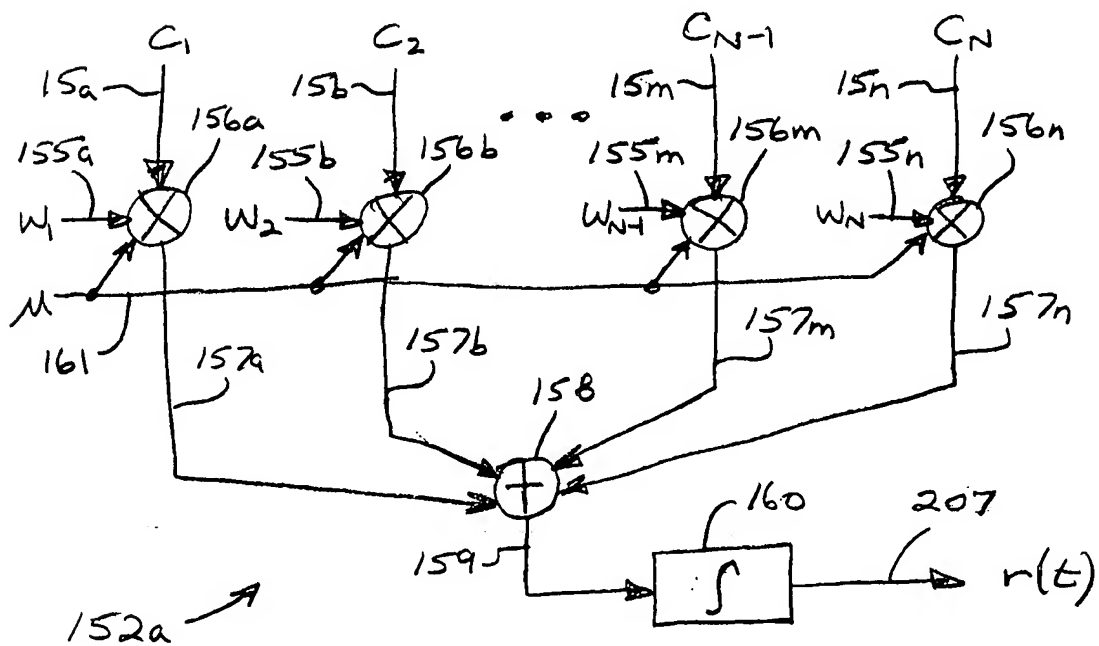


FIGURE 12



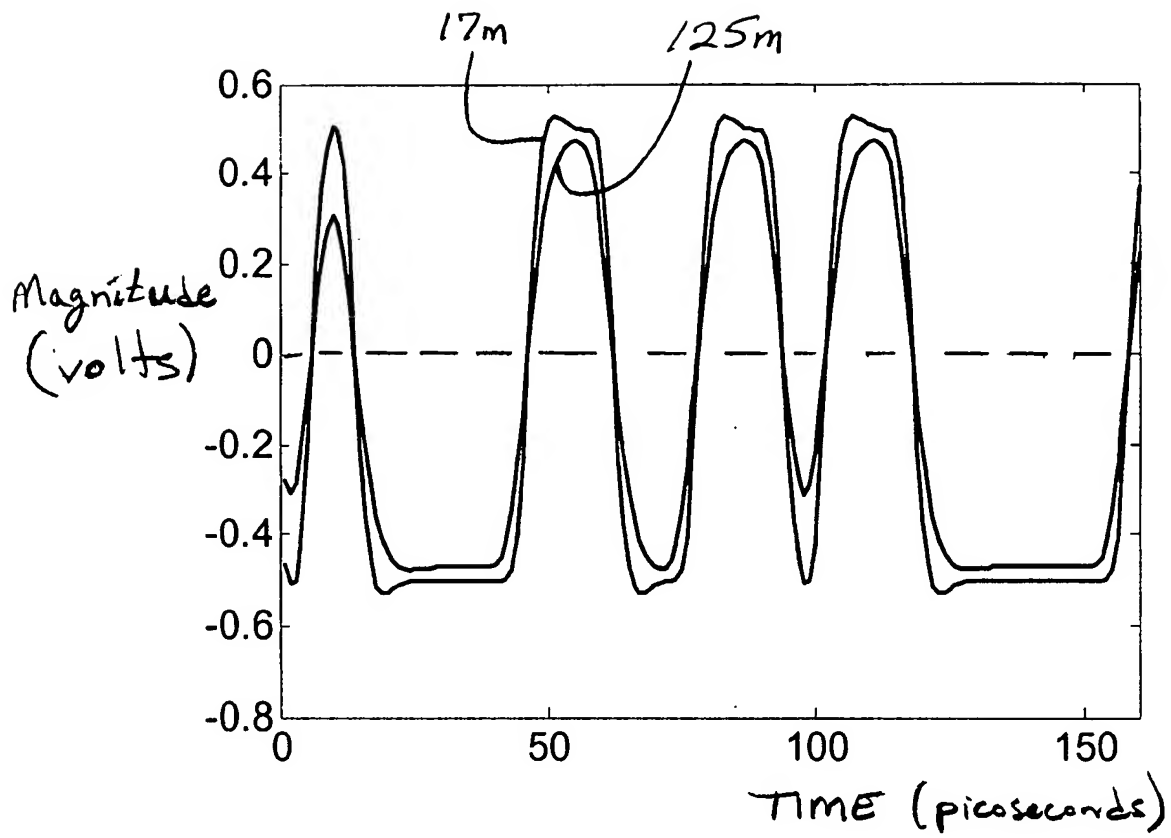


FIGURE 13